

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventors: Buswell et al.

Examiner:

Culbert, R.

Serial No:

10/061,492

**Group Art Unit:** 

1763

Filing Date: 1/31/02

**Confirmation No:** 

2065

Title:

Methods and Systems for Forming Slots in a Semiconductor

Substrate Containing Microelectronics

To:

Commissioner of Patents and Trademarks

PO Box 1450

Alexandria, VA 22313-1450

From:

Hewlett-Packard Development Company, L.P

Intellectual Property Administration

OK TO ENTER

P.O. Box 272400

Fort Collins, Colorado 80527-2400

AMEND MENT

Asta Callet 10/26/04 Roberts Calbert

## RESPONSE AFTER FINAL REJECTION

## INTRODUCTORY COMMENTS

This communication is in response to the Final Office Action dated 8/05/04, for which a three-month shortened statutory period for response is set for 11/05/04. Applicant submits this communication within a two month time period and respectfully requests that this communication be entered and that a responsive advisory action be generated. Please amend the above-identified application as set forth below.

Amendment to the Claims are reflected on the list of claims which begins on page 2 of this paper.

Remarks/Arguments begin on page 4 of this paper.